

Bs/DSEPHY-5(a) (AK)

2 0 2 4

(CBCS)

(5th Semester)

PHYSICS

Paper : DSEPHY-5(a)

(Digital and Analog Circuits and Instrumentation)

(PART : A—OBJECTIVE)

(Marks : 25)

KEY ANSWERS FOR OBJECTIVES

SECTION—I

(Marks : 10)

Put a Tick (✓) mark against the correct answer in the brackets provided : 1×10=10

1. (b) 0
2. (d) NAND gate
3. (b) 4
4. (b) To convert AC into DC
5. (b) To store and filter charge

/336K

(2)

- 6. (a) higher efficiency
- 7. (b) Common Emitter (CE)
- 8. (c) 9
- 9. (c) To find the Q-point of the transistor
- 10. (a) Infinite gain
- 11. (b) zero
- 12. (c) Phase shift of 180°
- 13. (a) $f = \frac{1}{2 RC}$
- 14. (b) Pin 2
- 15. (c) oscillates between high and low states

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